

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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SUMAN DATTA

ANNALISA CAPPELLANI ROBERT S. CHAU

Serial No.: 10/828,958

Filed: April 20, 2004

For: A METHOD FOR MAKING A

SEMICONDUCTOR DEVICE HAVING A HIGH-K GATE DIELECTRIC LAYER AND A METAL GATE ELECTRODE

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Art Unit: 2812

Examiner: unknown

Attorney Docket: P18609

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made - 1 -LJV/cak (10/01/97)

and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: Nov. 22, 2004

Mark V. Seeley Reg. No. 32,299

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FIRST CLASS CERTIFICATE OF MAILING

(37 C.F.R. § 1.8(a))

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Form PTO-1449 (Modified) Atty Docke							et No	t No.: P18609		Serial No.: 10/828,958					
List of Patents and Publications Statement (Use several sheets if necessary)									Applicant: Justin K. Brask et al.						
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